



IDESA Training Course Certificate

This is to certify that

Frank Poppen

has attended the following IDESA training course

Design for Manufacturing flow

5 - 8 May 2008

held at

Interuniversitair Micro-Electronica Centrum, Belgium

The 4-day DFM course introduced the advanced IC manufacturing flow, and focused upon defectivity, post-layout manufacturability issues such as OPC (optical proximity correction) and included litho simulations and process window issues.

After these process oriented topics, the course addressed design and layout for litho manufacturability, with proper attention for the back-end dual damascene copper interconnect issues. There were hands-on sessions on DFM solutions and Yield analysis, including Design-for-Test and Yield management.

IDESA Course Administration



IDESA

Implementation of widespread IC design skills in advanced deep submicron technologies at European Academia

www.idesa-training.org



EUROPRACTICE

Project partners: IMEC, Belgium; Ecole Polytechnique Fédérale de Lausanne, Switzerland; Technische Universiteit Delft, The Netherlands; Warsaw University of Technology, Poland; Katholieke Universiteit Leuven, Belgium; Commissariat à l'Energie Atomique, France; Slovak University of Technology in Bratislava, Slovak Republic; STFC Rutherford Appleton Laboratory, UK.

